

Drop-In

Monolithic Amplifier

DC-6 GHz

Product Features

- DC-6 GHz
- Single voltage supply
- Internally matched to 50 ohms
- Unconditionally stable
- Low performance variation over temperature
- Transient protected
- Aqueous washable
- Protected By US Patent 6,943,629

Typical Applications

- Cellular/ PCS/ 3G Base Station
- CATV, Cable Modem & DBS
- Fixed Wireless & WLAN
- Microwave Radio & Test Equipment



ERA-2+

CASE STYLE: VV105
PRICE: \$ 1.52ea. QTY. (30)

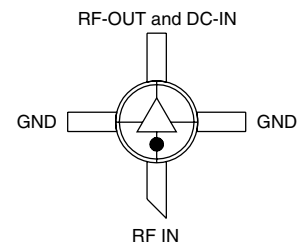
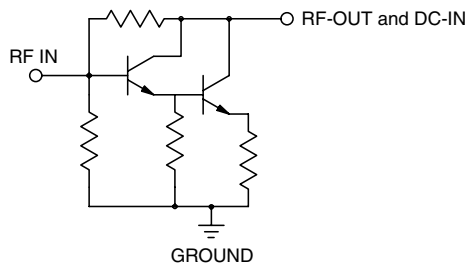
+ RoHS compliant in accordance with EU Directive (2002/95/EC)

The +Suffix has been added in order to identify RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications.

General Description

ERA-2+ (RoHS compliant) is a wideband amplifier offering high dynamic range. It has repeatable performance from lot to lot. It is enclosed in a Micro-X package. ERA-2+ uses Darlington configuration and is fabricated using InGaP HBT technology. Expected MTBF is 9100 years at 85°C case temperature.

simplified schematic and pin description



Function	Pin Number	Description
RF IN	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
RF-OUT and DC-IN	3	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit".
GND	2,4	Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.

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RF/IF MICROWAVE COMPONENTS

REV. L
M108520
ERA-2+
070119
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Electrical Specifications at 25°C and 40mA, unless noted

Parameter	Min.	Typ.	Max.	Units	Cpk	
Frequency Range*	DC		6	GHz		
Gain	f=0.01 GHz	15.4	16.4	17.0	dB	≥ 1.5
	f=0.1 GHz	15.4	16.4	17.0		
	f=1 GHz		15.8			
	f=2 GHz	13.7	14.9	15.3		
	f=3 GHz		13.9			
	f=4 GHz	11.9	12.5	12.9		
	f=6 GHz		10.7			
Magnitude of Gain Variation versus Temperature (values are negative)	f=0.1 GHz		.0025	.005	dB/°C	
	f=1 GHz		.003	.006		
	f=2 GHz		.0035	.007		
	f=3 GHz		.0045	.009		
	f=4 GHz		.0045	.009		
	f=6 GHz		.006	.011		
Input Return Loss	f=0.1 GHz		30	dB		
	f=3 GHz		25			
	f=6 GHz		22			
Output Return Loss	f=0.1 GHz		25	dB		
	f=3 GHz		16			
	f=6 GHz		14			
Reverse Isolation	f=3 GHz	18	20	dB		
Output Power @ 1 dB compression	f=0.1 GHz		13.1	dBm	≥ 1.33	
	f=2 GHz	11	13			
	f=4 GHz		11			
Saturated Output Power (at 3dB compression)	f=0.1 GHz		14	dBm		
	f=2 GHz		13			
	f=4 GHz		12			
Output IP3	f=0.1 GHz	25	29	dBm	≥ 1.33	
	f=2 GHz	25	29			
	f=4 GHz	20	25			
Noise Figure	f=0.1 GHz		3.2	3.7	dB	≥ 1.33
	f=2 GHz		3.3	3.8		
	f=4 GHz		3.4	4		
Group Delay	f=2 GHz		80	psec		
Recommended Device Operating Current			40	mA		
Device Operating Voltage	3.20	3.4	3.6	V	≥ 1.5	
Device Voltage Variation vs. Temperature at 40mA		-2.5		mV/°C		
Device Voltage Variation vs. Current at 25°C		8.1		mV/mA		
Thermal Resistance, junction-to-case ¹		155		°C/W		

*Guaranteed specification DC-6 GHz. Low frequency cut off determined by external coupling capacitors.

Absolute Maximum Ratings

Parameter	Ratings
Operating Temperature*	-45°C to 85°C
Storage Temperature	-65°C to 150°C
Operating Current	75mA
Power Dissipation	330mW
Input Power	15dBm

Note: Permanent damage may occur if any of these limits are exceeded.

These ratings are not intended for continuous normal operation.

¹Case is defined as ground leads.

*Based on typical case temperature rise 5°C above ambient.



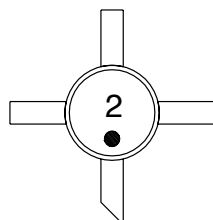
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RF/IF MICROWAVE COMPONENTS

Product Marking



Additional Detailed Technical Information

Additional information is available on our web site. To access this information enter the model number on our web site home page.

Performance data, graphs, s-parameter data set (.zip file)

Case Style: VV105

Plastic micro-x, .085 body diameter, lead finish: tin/silver/nickel

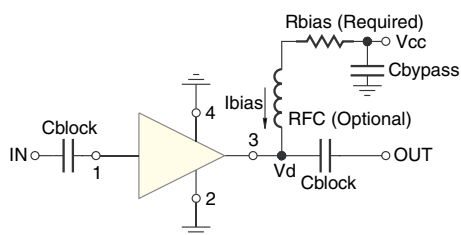
Tape & Reel: F20

Suggested Layout for PCB Design: PL-261

Evaluation Board: TB-431-2+

Environmental Ratings: ENV08T2

Recommended Application Circuit



Test Board includes case, connectors, and components (in bold) soldered to PCB

R BIAS	
Vcc	"1%" Res. Values (ohms) for Optimum Biasing
7	88.7
8	113
9	137
10	162
11	187
12	213
13	237
14	261
15	287
16	309
17	332
18	365
19	392

ESD Rating

Human Body Model (HBM): Class 1A (250 v to < 500 v) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): Class M1 (< 100 v) in accordance with ANSI/ESD STM 5.2 - 1999

MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDECJ-STD-020C

No.	Test Required	Condition	Standard	Quantity
1	Visual Inspection	Low Power Microscope Magnification 40x	MIP-IN-0003 (MCT spec)	45 units
2	Electrical Test	Room Temperature	SCD (MCL spec)	45 units
3	SAM Analysis	Less than 10% growth in term of delamination	J-Std-020C (Jedec Standard)	45 units
4	Moisture Sensitivity Level 1	Bake at 125°C for 24 hours Soak at 85°C/85%RH for 168 hours Reflow 3 cycles at 260°C peak	J-Std-020C (Jedec Standard)	45 units

MSL Test Flow Chart

